

# Nomination: IEEE Solid-State Circuits Society Japan Chapter | 948037929480

Started at: 11/30/2016 8:09 PM - Finalized at: 11/30/2016 11:37 PM

Round: Main

## Page: Chapter Information

Question	Answer
Chapter Name (Full Name)	IEEE Solid-State Circuits Society Japan Chapter
Chapter Chair First Name	Takeshi
Chapter Chair Last Name	Yamamura
Chapter Chair Email Address	yamamura.takesh@jp.fujitsu.com
Submitter First Name If submitting for Chapter Chair	Takeshi
Submitter Last Name If submitting for Chapter Chair	Yamamura
Submitter Email Address	yamamura.takesh@jp.fujitsu.com
Your Position in Chapter	
Chapter Street Address	Street: 4-1-1 Kamikodanaka Line2: Nakahara-ku City: Kawasaki CountryCode: Japan State: Kanagawa Zip: 211-8588
Chapter Website	<a href="http://www.ieee-jp.org/section/tokyo/chapter/SSC-37/ssc.htm">http://www.ieee-jp.org/section/tokyo/chapter/SSC-37/ssc.htm</a>

## Page: Annual Chapter Subsidy Request

Question	Answer
Amount of Annual Chapter Subsidy request  Maximum amount \$2,000/year (USD). (\$1,000/year for joint chapters)	\$2,000
Date (month/year) of most recent annual subsidy received	04/2016
Total amount (USD) of most recent annual subsidy received from SSCS	\$2,000
Total Chapter Budget (USD) for 2016	\$2,000
Total Chapter Budget (USD) for previous year (2015).	\$2,000
Note: Budgets above should include all sources of support from SSCS, your Section, Industrial sources and other societies in case of joint chapter	
Do you plan to apply for a Fall Extra Chapter Subsidy (Yes or No)  If yes, please note you will need to separately apply for the Fall Extra Chapter Subsidy later in November.	Yes
Joint Chapter (Yes or No)	No
Student Branch Chapter (Yes or No)	No

<p>Brief description of activity planned using the requested funds</p>	<p>### Sponsored Meetings</p> <p>(1) Report of 2017 VLSI Symposia on Technology and Circuits Japanese presenters in 2017 VLSI SYmposia on Technology and Circuits are invited to give a presentation in Japanese to share the state-of-the-art circuit technology with VLSI engineers and researchers. About 20 presenters are invited.</p> <p>(2) Report of ASSCC 2017 to be co-sponsored with SSCS Kansai Chapter Japanese presenters in ASSCC 2017 are invited to give a presentation in Japanese to share the state-of-the-art technology with VLSI engineers and researchers. About 20 presenters are invited.</p> <p>(3) Meeting by Distinguished Lecturers We invite a couple of Distinguished Lecturers to give presentations to VLSI engineers and researchers.</p> <p>### Awards</p> <p>For membership promotion and encouraging research activities of students, two awards are offered.</p> <p>(1) IEEE SSCS Japan Chapter Academic Research Award Two students who did an outstanding research in the VLSI field will be selected and awarded</p> <p>(2) IEEE SSCS Japan Chapter VDEC Design Award One students who did an outstanding research in the VLSI field will be selected and awarded</p>
<p>Explain benefits for Chapter and SSCS (such as increasing SSCS membership) from above activities</p>	<p>### Sponsored Meetings</p> <p>From our statistics, less than 40% of attendees of our sponsored report meetings are not a member of IEEE. We, every time before starting the meetings, advertise the benefits of being the member of IEEE and SSCS. They are the best opportunities to increase SSCS membership.</p> <p>### Awards</p> <p>In Japan, students, who are awarded by any institutions such as IEEE, are eligible to apply exemption of repayment of student loan. Awarding students are good advertisement for attracting the interest in IEEE and finally increasing SSCS membership.</p>
<p>Budget details for above activity</p>	<p>### Sponsored Meetings</p> <p>(1) Report of 2017 VLSI Symposia on Technology and Circuits \$900 (conference room fee and rewards for conference supporters)</p> <p>(2) Report of ASSCC 2017 to be co-sponsored with SSCS Kansai Chapter \$500 (conference room fee and rewards for conference supporters)</p> <p>(3) Meeting by Distinguished Lecturer \$300 (conference room fee and rewards for conference supporters)</p> <p>### Awards</p> <p>(1) IEEE SSCS Japan Chapter Academic Research Award \$200</p> <p>(2) IEEE SSCS Japan Chapter VDEC Design Award \$100</p>

Brief description of previous year's activities. (include number of Chapter meetings, DL's, educational activities, conference short courses, tutorials, and any activities to increase SSCS membership.

### ### Sponsored Meetings

#### (1) Report of ISSCC 2016

Japanese presenters in ISSCC 2016 were invited to give a presentation in Japanese to share the state-of-the-art technology with VLSI engineers and researchers. About 12 presenters were invited. We invited Mr. Nakamura of NTT Docomo to give a presentation on the future 5G wireless technology.

#### (2) Report of ASSCC 2016

Japanese presenters in ASSCC 2016 were invited to give a presentation in Japanese to share the state-of-the-art technology with VLSI engineers and researchers. About 12 presenters were invited. We invited Professor Asad Abidi of UCLA to give a presentation on non-linear PLL design.

#### (3) Tutorials

We invited Professor Asad Abidi of UCLA to give a presentation on non-linear PLL design.

### ### Technically Co-sponsored Meetings

(4) Technical Group Meeting on Analog RF Circuits sponsored by IEICE Integrated Circuits and Devices Technical Group

(5) Technical Group Meeting on Mili-meter-wave Circuits sponsored by IEICE Integrated Circuits and Devices Technical Group

(6) Technical Group Meeting on Memory Circuits sponsored by IEICE Integrated Circuits and Devices Technical Group

(7) Symposium on Analog VLSI sponsored by The Institute of Electrical Engineering

(8) LSI and System Workshop sponsored by IEICE Integrated Circuits and Devices Technical Group

(9) Technical Group Meeting on Analog and Sensor Circuits sponsored by IEICE Integrated Circuits and Devices Technical Group

(10) Summer School on VLSI sponsored by Japan Society for Promotion of Science

(11) VLSI Designer's Forum sponsored by VLSI Design and Educational Center(VDEC)

(12) Design to Test Symposium sponsored by VLSI Design and Educational Center(VDEC)

(13) Technical Group Meeting on High Performance Computing sponsored by IEICE Integrated Circuits and Devices Technical Group

(14) Summer School on Analog RF Circuits sponsored by IEICE Integrated Circuits and Devices Technical Group

(15) Internal Workshop on Image Sensors and Imaging Systems sponsored by Optical Society Japan

(16) Technical Group Meeting for young researchers sponsored by IEICE Integrated Circuits and Devices Technical Group

### ### Awards

#### (1) IEEE SSCS Japan Chapter Academic Research Award

Two students who did an outstanding research in the VLSI field were selected and awarded @LSI and Systems Workshop sponsored by IEICE Integrated Circuits and Devices Technical Group

#### (2) IEEE SSCS Japan Chapter VDEC Design Award

One student who did an outstanding research in the VLSI field were selected and awarded

	@VLSI Designer's Forum sponsored by VLSI Design and Educational Center(VDEC)
Report use of SSCS funds for last year	N/A
Report of support of student activities at local institutions	<p>### Awards</p> <p>- IEEE SSCS Japan Chapter Academic Research Award Two students who have promoted an outstanding research in the VLSI field were selected and awarded.</p> <p>- IEEE SSCS Japan Chapter VDEC Design Award One student who are promoting and give a presentation of an outstanding research in the VLSI field were selected and awarded.</p>
How would you describe the growth of your Chapter over the last few years, Please type in one of the follow: High Growth, Slow Growth, Constant, or Reducing	Constant
Total number of members expected by end of 2016	820
Total number of members at end of 2015	820
Number of SSCS members you nominated this year for IEEE Senior Member	1
Number of SSCS members you nominated this year for IEEE Fellow	1
Number of SSCS members you nominated last year for IEEE Senior Member	0
Number of SSCS	0

members you  
nominated last year for  
IEEE Fellow

## Page: Bank Information

Question	Answer
Name of Bank	Mizuho Bank
Street Address	1-6-1 Takezono, Tsukuba Mitsui Building 5F
City	Tsukuba
State/Province	Ibaraki
Country	Japan
Name on Bank Account	IEEE SSCS J-CHAPTER
NOTE: Account name must include Chapter, Section, or University name. Personal accounts cannot be used.	
Bank Account Number	1119878
SWIFT or ABA Code	
Special instructions (including intermediary bank, if applicable)	
Concentration Banking or Custody Account Number (if preferred). Also known as HOP Number	
Name on Concentration Banking or Custody Account	
If Chapter located in India, provide IFSC Code	
If Chapter located in India, provide MIRC	

Code